



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DIGITAL SIGNAL PROCESSING LABORATORY

Overview:

- Digital Signal processing is the root of many advanced technologies nowadays, including telecommunication, internet, multimedia and biomedical information processing.
- The area of laboratory is 145.15 Sq.m (54.67*28.58)

List of Software's:

- Mentor Graphics- HEP Category 1
- Mentor Graphics- HEP Category 2

List of equipments:

1. Personal Computer with Intel Core i5 Processor ,8 GB RAM 1TB HDD
2. Personal Computer with Pentium Dual core Processor ,4GB RAM,500 GB HDD
3. Xilinx Spartan 3e FPGA Trainer Kit (VSK-Spartan 3E)
4. Cool Runner-II Piggy Back(VVSP 08)
5. Add-On Card For VLSI Kits(VVSI 29)
6. 6 Digits 7 Segment Display Board(VVSI-06)
7. Traffic Light Control System(TLC)
8. Spartan 3e Piggy Back(VVSP-14)
9. ALS-SDA-320C50M based DSP Trainer Kit
10. TMS320C5416 based DSP Trainer Kit
11. TMS320VC33 based DSP Trainer Kit
12. TMS320C5416 based Advanced DSP Trainer Kit [VSK-5416]
13. TMS320VC5416 based DSP Trainer Kit [VSK-5416]
14. TMS320C6713 based Advanced DSP Trainer Kit [VSK-6713] & Universal VLSI Trainer



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Snapshot of digital signal processing laboratory